

SPECIFICATION

REFOND P/N

RF-AURB14TS-AA-B

R&D

Mass Production



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1. Description

1.1 General Description



The source color devices are made with AlGaInP on Substrate Light Emitting Diode Product
 Package: 2.2mmX1.4mmX1.3mm.

橙 LED AlGaInP

2.2mmX1.4mmX1.3mm

1.2 Features

PLCC2 Package. PLCC2

Extremely wide viewing angle.

Suitable for all SMT assembly and solder process. SMT

Available on tape and reel.

Moisture sensitivity level: Level 2. Level2

Compliance with RoHS and REACH. RoHS REACH

Qualifications: The product qualification test plan is based on the guidelines of AEC-Q101
 Stress Test Qualification for Automotive Grade Discrete Semiconductors

AEC-Q101

1.3 Application

Automotive Lighting Interior.



1.4 Package Dimension



Notes

1. All dimensions units are millimeters.
2. All dimensions tolerances are

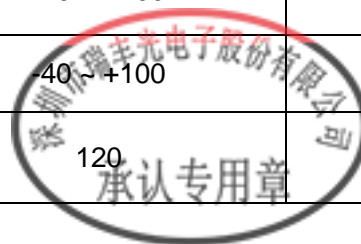
1.5 Product Parameters

Table 1-1 Electrical / Optical Characteristics at Ts=25°C

Item	Symbol	Test Condition	Value			Unit
			Min.	Typ.	Max.	
Forward Voltage	V_F	$I_F=5mA$	1.7	1.8	2.3	V
Reverse Current	I_R	$V_R=5V$	---	---	10	μA
Luminous Intensity	I_V	$I_F=5mA$	65	100	120	mcd
Dominant wavelength	WD	$I_F=5mA$	602.5	605	610	nm
Viewing Angle	2 1/2	$I_F=5mA$	---	120	---	deg
Thermal Resistance.	R_{THJ-S}	$I_F=5mA$	---	---	300	/W

Table 1-2 Absolute Maximum Ratings at Ts=25°C

Parameter	Symbol	Rating	Unit
Power Dissipation	P_D	69	mW
Forward Current	I_F	30	mA
Peak Forward Current	I_{FP}	100	mA
Reverse Voltage	V_R	5	V
Electrostatic Discharge (HBM)	V_{ESD}	2000	V
Operating Temperature	T_{OPR}	-40 ~ +100	
Storage Temperature	T_{STG}	-40 ~ +100	
Junction Temperature	T_J	120	



Notes

1. 1/10 Duty cycle, 10ms pulse width. 10ms, 1/10.
2. The above forward voltage measurement allowance tolerance is $\pm 0.1V$. $\pm 0.1V$.
3. The above color coordinates measurement allowance tolerance is ± 0.005 . ± 0.005 .
4. The above luminous intensity measurement allowance tolerance $\pm 10\%$. $\pm 10\%$.
5. Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
6. All measurements were made under the standardized environment of Refond.
7. When the LEDs are in operation the maximum current should be decided after measuring the package temperature, junction temperature should not exceed the maximum rate. LED
8. ESD yield is over 90% at 2000V ESD (HBM). ESD protection during products handling is needed. 90% LED ESD2000V

1.6Bin Range Of Forward Voltage and Luminous Intensity and Dominant Wavelength (IF=5mA) BIN (IF=5mA)

Table 1-3

V _F	A2	B1	B2	C1	C2	D1
	1.7-1.8	1.8-1.9	1.9-2.0	2.0-2.1	2.1-2.2	2.2-2.3
IV(mcd)	F1	F2	G1			
	65-80	80-100	100-120			
WD(nm)	A2	B1	B2			
	602.5-605	605-607.5	607.5-610			



1.7 Typical Optical Characteristics Curves

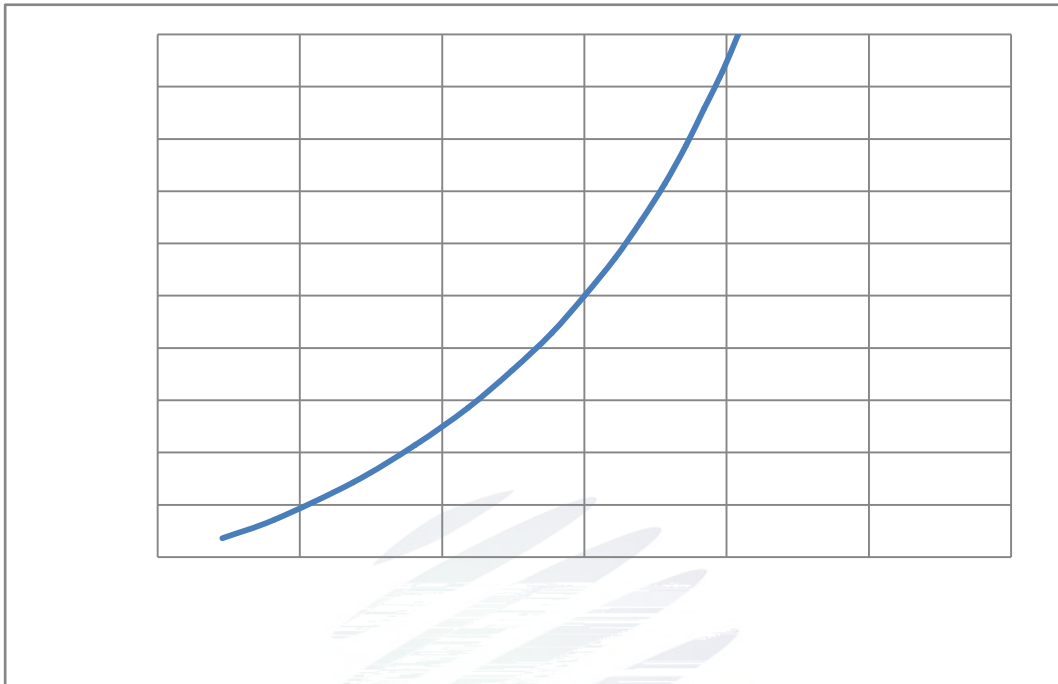


Fig. 1-6 Forward Voltage Vs Forward Current

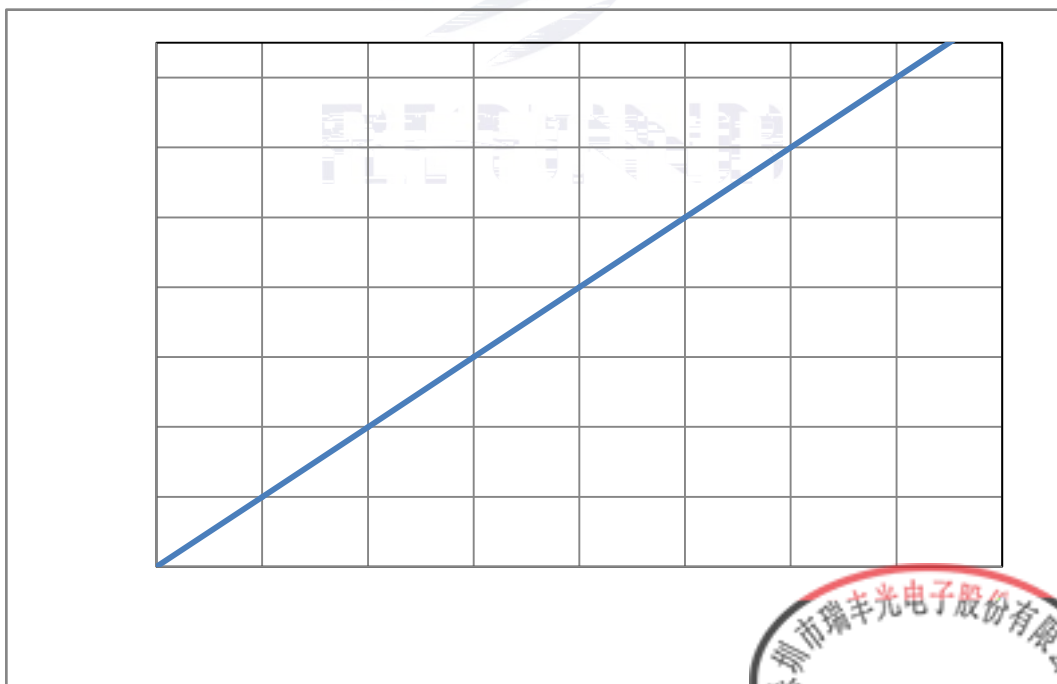


Fig. 1-7 Forward Current Vs Relative Intensity



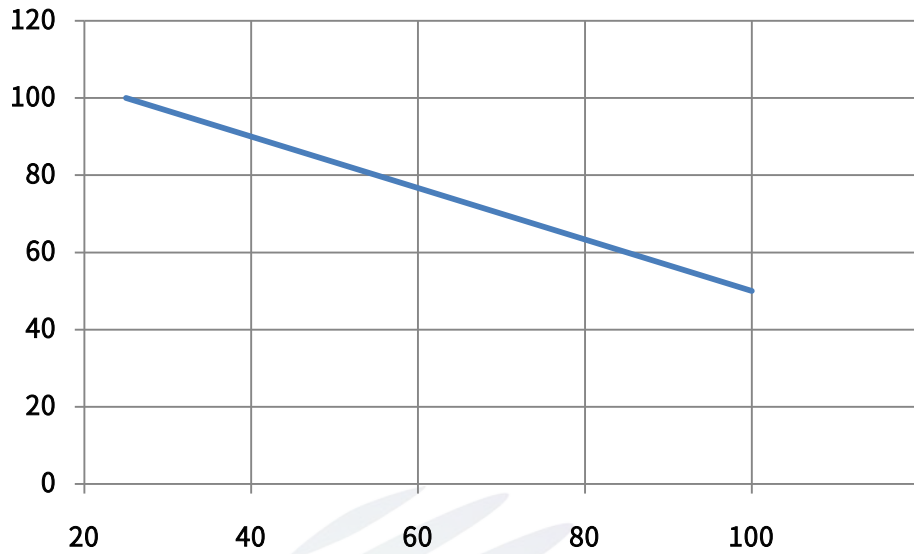


Fig. 1-8 Solder Temperature Vs Relative Intensity



Fig. 1-9 Solder Temperature Vs Forward Current

Tj 120

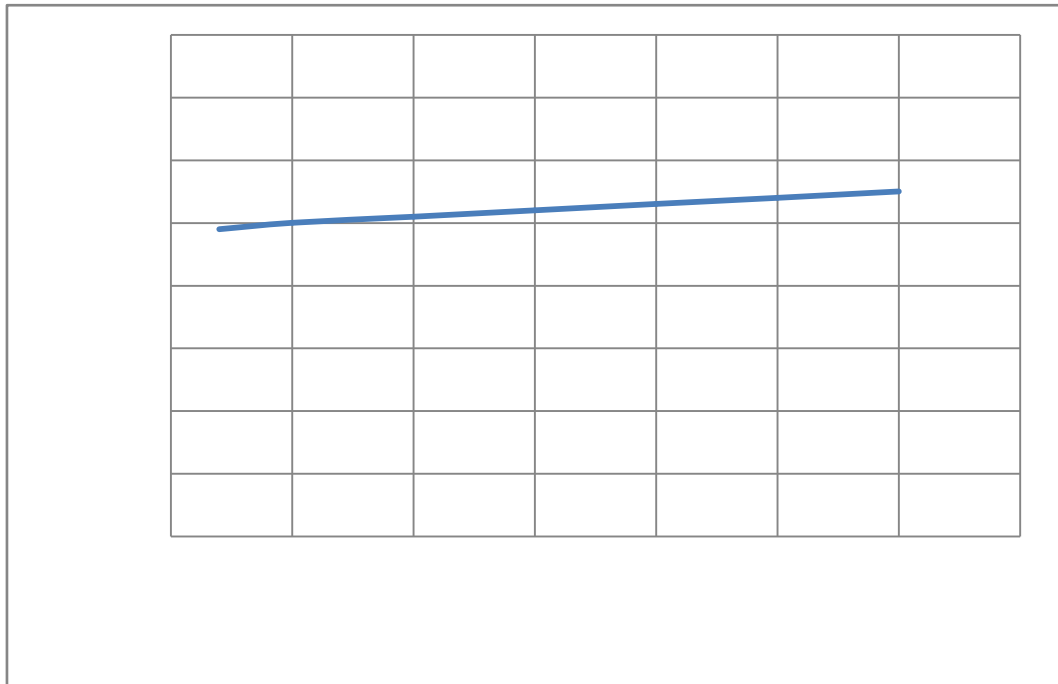


Fig. 1-12 Forward current VS Dominate wavelength (Ts=25°C)

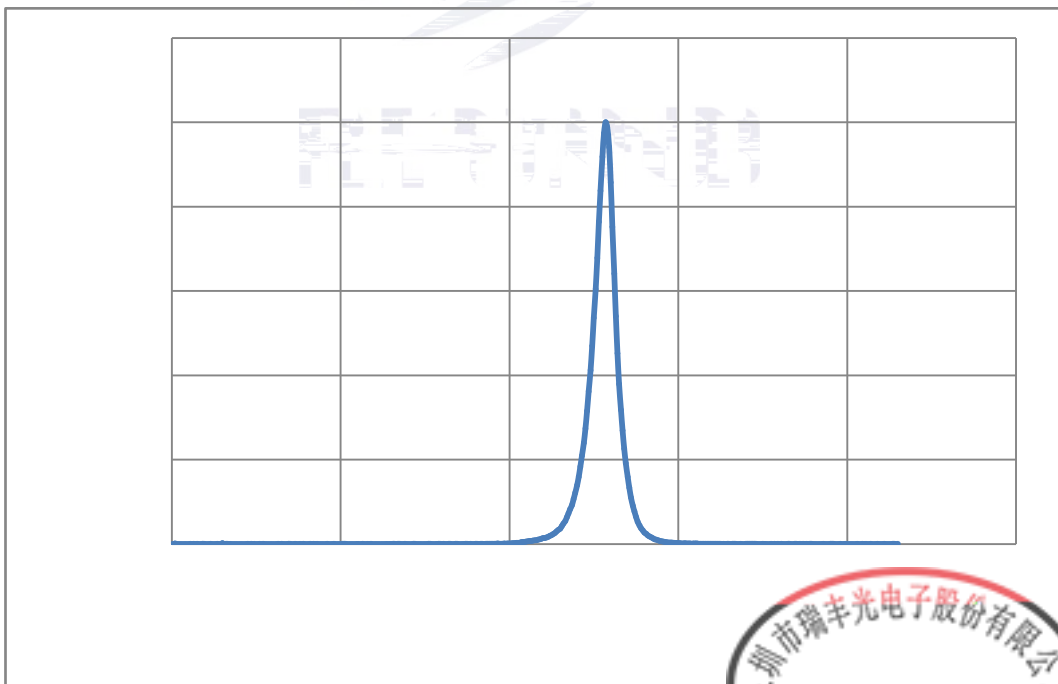


Fig. 1-13 Spectrum Distribution





2.1.3 Label Form Specification

Table 2-2 Specification

PART NO.	Part Number
SPEC NO.	Spec Number
LOT NO.	Lot Number
BIN CODE	Bin Code
IV	Luminous Intensity
XY	Chromaticity Bin
V _F	Forward Voltage
WLD	Wavelength
QTY	Packing Quantity
DATE	Made Date

Fig. 2-3 Label Form Specification

2.2 Moisture Resistant Packing

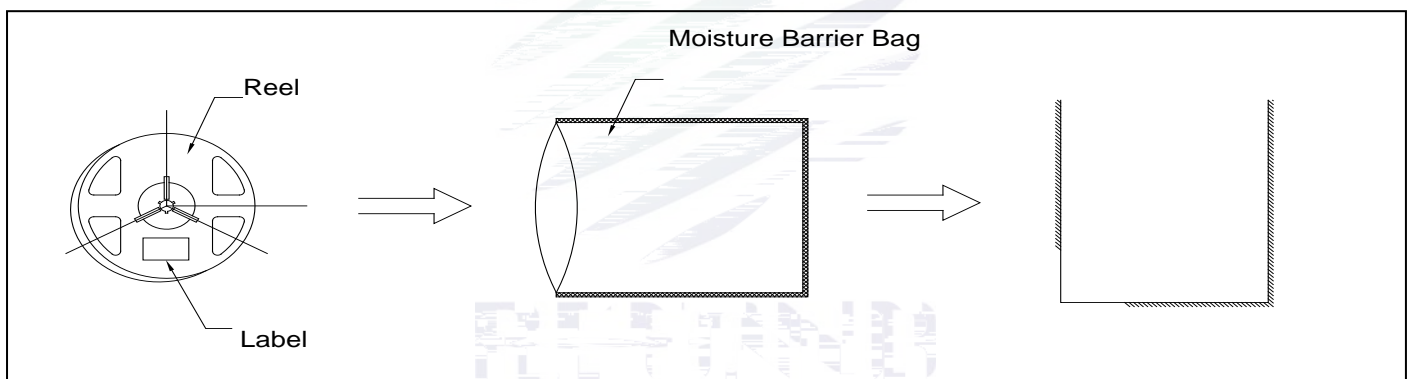


Fig.2-4 Moisture Resistant Packing

2.3 Cardboard Box

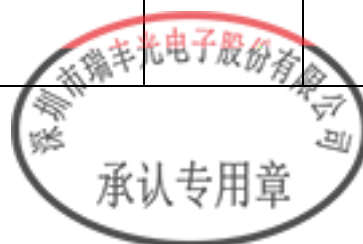


Fig.2-5 Cardboard Box

2.4 Reliability Test Items And Conditions

Table 2-3 Reliability Test Items And Conditions

Test Items	Ref. Standard	Test Condition	Time	Quantity	Ac/Re /
Reflow	JESD22-B106	Temp:260 max T=10 sec	2times	20pcs.	0/1
MSL2 2	JESD22-A113	85 / 60%R	168 hrs.	20pcs.	0/1
Thermal Shock	JEITAED-4701 300307	-40 15min 10s 125 15min	1000 cycle	20pcs.	0/1
Life Test	JESD22-A108	Ta=105 I _F =5mA	1000hrs.	20pcs.	0/1
High Temperature High Humidity Life Test	JESD22-A101	85 / 85%RH I _F =5mA	1000hrs.	20pcs.	0/1



2.5 Criteria For Judging Damage

Table 2-4 Criteria For Judging Damage

Test Items	Symbol	Test Condition	Criteria For Judgement	
			Min.	Max.
Forward Voltage	V_F	$I_F=5mA$	-	U.S.L*)x1.1
Reverse Current	I_R	$V_R = 5V$	-	U.S.L*)x2.0
Luminous Intensity	I_V	$I_F=5mA$	L.S.L*)x0.7	-

Notes

Notes

1.U.S.L: Upper standard level

L.S.L: Lower standard level

2.The above reliability tests is based on the verification of a single/strip LED of Refond's existing experimental Platform, the reliability experiment was taken under good heat dissipation conditions. When customers applies The LED to the series and parallel circuit, should take consideration of all the factors such as the current, Voltage distribution, heat dissipation and others. / LED

LED

3.The technical information shown in the data sheets is limited to the typical characteristics and circuit examples of the referenced products. It does not constitute the warranting of industrial property nor the granting of any license.





Notes

(1)Reflow soldering should not be done more than twice. If more than 24 hours between the two soldering,LED will be damaged. 24 LED

(2)When soldering , do not put stress on the LEDs during heating.

3.1.1 Soldering Iron

(1) When do soldering by hand, keep the temperature of iron below less 300 less than 3 seconds. 300 3

(2) Soldering by hand should be done only one time.

3.1.2 Repairing

Repairing should not be done after the LEDs have been soldered. When repairing is unavoidable,a double-head soldering iron should be used (as below figure). It should be confirmed in advance whether the characteristics of LEDs will or not be damaged by repairing.

LED

LED

3.1.3 Cautions

(1) The encapsulated material of the LEDs is silicone. Therefore the LEDs have a soft surface on the top of package. The pressure to the top surface will be impacted on the reliability of the LEDs. Precautions should be taken to avoid the strong pressure on the encapsulated part. So when use the picking up nozzle, the pressure on the silicone resin should be proper. LED LED

(2) Components should not be mounted on warped (non coplanar) portion of PCB. After soldering, do not warp the circuit board.LED PCB

(3) Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering. Do not rapidly cool device after soldering.



4. Handling Precautions

4.1 Handling Precautions

(1) LED operating environment and sulfur element composition cannot be over 100PPM in the LED mating usage material. This is provided for informational purposes only and is not a warranty or endorsement.

LED

100PPM

(2) In order to prevent external material from getting into the inside of LED, which may cause the malfunction of LED, the single content of Bromine element is required to be less than 900PPM, the single content of Chlorine element is required to be less than 900PPM, the total content of Bromine element and Chlorine element in the external materials of the application products is required to be less than 1500PPM. This is provided for informational purposes only and is not a warranty or endorsement.

LED

900PPM

900PPM

1500PPM

(3) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues. Refond advises against the use of any chemicals or materials that have been found or are suspected to have an adverse affect on device performance or reliability. To verify coel (f)i4()11(T)2(o)-12(,)2(o-0.006 Tw -44.739 -2.091 6)8(re)it ad.52[(e)itsp)18]TJ

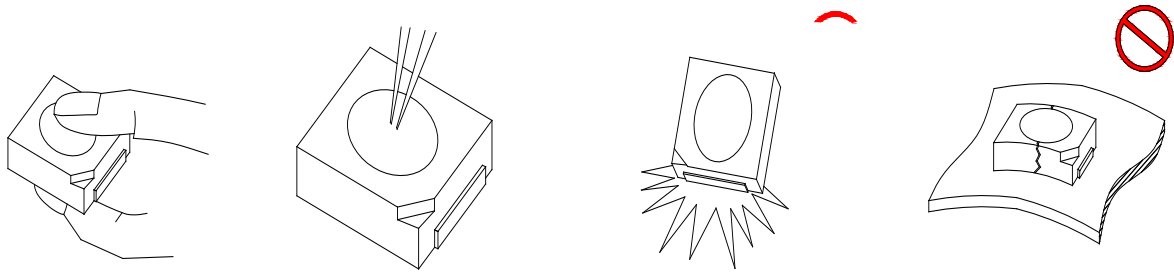


Fig 4-1 Handling Precautions

(5) In designing a circuit, the current through each LED can not exceed the absolute maximum rating specified for each LED. In the meanwhile, resistors for protection should be applied, otherwise slight voltage shift will cause big current change, burn out may happen. The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.

LED

LED

(6) Thermal Design is paramount importance because heat generation may result in the Characteristics decline, such as brightness decreased, Color change and so on. Please consider the heat generation of the LEDs when making the system design.

LED

(7) Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust, requiring special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components. Refond suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.

LED

Table 4-1 Storage

Conditions		Temperature	Humidity	Time
Storage	Before Opening Aluminum Bag	30	75%	Within 1 Year From Date
	After Opening Aluminum Bag	30	60%	Recommended for use within 24 hours 24
Baking		60± 5	-	24hours
				24

(8) If the moisture absorbent material silica gel has faded away or the LEDs have exceeded the storage time, baking treatment should be performed after unpacking and based on the following condition(60±5) for above 24 hours.

24

If the package is flatulence or damaged, please notify the sales staff to assist.

(9) Similar to most Solid state devices; LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS). LED

(10) Other points for attention, please refer to our relevant information.



Version History/

Date	Revisor	Version	Verifier	Remarks
2021/04/12	Huang Nianhua	E0	Zhu Yiming	Newissue
2023/02/02	Chen Weixiong	E1	Zhu yiming	Template update





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Declare

This specification is written both in English and in Chinese and the latter is formal.